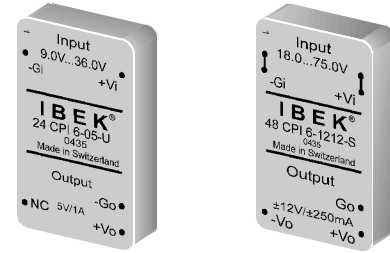


Single & double outputs
Input to output isolated

- Ultra wide 1:4 input voltage ranges
- Very high efficiency up to 86%
- Very low output voltage noise
- High reliability
- Continuous no-load and short-circuit proof
- Extended ambient temperature range -40...+85°C
- DIL 24 case, h = 0.41"
- Made in Switzerland, accordingly ISO 9001:2000
- 2 years warranty

NEW!



Electromagnetic emission EN 55022 < A
Conducted and radiated emissions

	24 CPI	48 CPI	
No load input current	12	10	mA typ.
Switching frequency	400 kHz typ.		
Output voltage accuracy	± 1% max.		
Static line regulation	± 0.4% max.		
Static load regulation FL to 10%	0...0.4% max.		
Output voltage noise (20 MHz BW)	50 mVpp typ.		
Efficiency	81% to 86% typ.		
I/O Electric strength test voltage	1500 VDC		
Coupling capacitance	1000 pF		
Ambient temperature range	-U: -40...+71°C; -S: -40...+85°C ¹		
Case temperature range	-U: -40...+95°C; -S: -40...+105°C		
Storage temperature range	-U: -55...+105°C; -S: -55...+105°C		
Case material	Non-conductive plastic, UL94 V-0 rated		
MTBF, GB @ 40°C case (MIL-HDBK-217F, Notice 2)	> 2.300.000 hours		

¹ Power derating 4%/°C above 80°C

Standard Types ²	Input voltage VDC	Output	
		VDC	mA
Single output	9...36 (nominal 24) 45 V for 10 ms repetition rate 10 s	3.3	1500
xx CPI6-03-U		5	1000
xx CPI6-05-U		12	500
xx CPI6-15-U		15	400
Double output ³	18...75 (nominal 48)	± 3.3	± 600
xx CPI6-0303-U		± 5	± 500
xx CPI6-0505-U		± 12	± 250
xx CPI6-1212-U		± 15	± 200

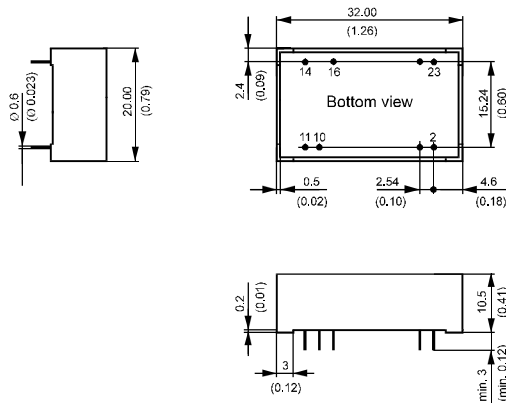
² To complete type number replace "xx" by the required nominal input voltage, e.g. 24 CPI6-05-U.

³ Double outputs should have symmetrical load conditions to comply with the specifications listed on the left side.

Extended temperature range -S is available for all types. Replace -U by -S on type number, e.g. 48 CPI6-1212-S.

Modifications and customs are available upon request.

Dimensions in mm (inches)



Pin Configuration		
Pin	Single output	Double output
2	-Gi	-Gi
3	No Pin	-Gi
10	NC	No Pin
11	No Pin	-Vo
14	+Vo	+Vo
16	-Go	Go
22	No Pin	+Vi
23	+Vi	+Vi