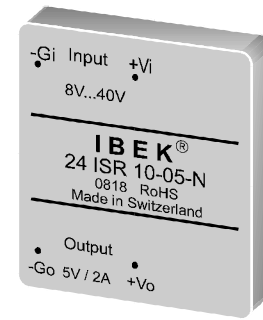


**Single outputs  
Non-isolated**

- Ultra wide input voltage ranges
- Very high efficiency, typical 87%
- Inhibit input (optional)
- Excellent dynamic load regulation
- Continuous no-load and short-circuit proof
- Extended ambient temperature range -40...+85°C
- 2" x 2" case, h = 0.41"
- Made in Switzerland, accordingly ISO 9001:2000
- 2 years warranty



**Electromagnetic emission EN 55022 < B**  
Conducted and radiated emissions

No load input current	15 mA max.
Input current in inhibit mode	3 mA max.
Switching frequency	150 kHz typ.
Output voltage accuracy	± 1.5% max.
Static line regulation	± 0.3% typ.
Static load regulation FL to NL	0...0.7% typ.
Output voltage noise (20 MHz BW)	30 mVpp typ.
Efficiency	87% typ.
Dynamic load regulation	± 80 mV max. <sup>1</sup>
Load transient recovery time	100 µs typ. <sup>1</sup>
Inhibit input (i)	Open or apply -0.3...+0.8 VDC for normal operation. Apply +2.4...+5.5 VDC to switch off the output voltage.
Ambient temperature range	-N: 0...+71°C
- Extended	-T: -25...+71°C; -S: -40...+85°C
Case temperature range	-N: 0...+95°C
- Extended	-T: -25...+95°C; -S: -40...+105°C
Storage temperature range	All: -55...+105°C
Case material	Metal with plastic bottom cover, UL94 V-0 rated
MTBF, GB @ 40°C case	> 2.150.000 hours
(MIL-HDBK-217F, Notice 2)	

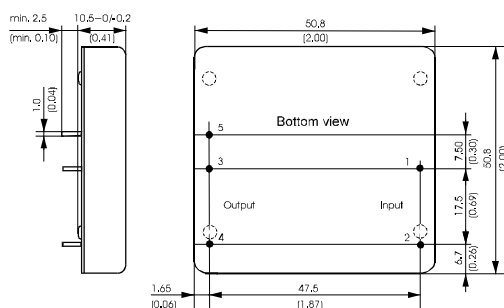
Standard Types	Input voltage VDC	Output	
		VDC	mA
Single output 24 ISR10-05-N RoHS	8...40 (nominal 24)	5	2000

Extended temperature ranges -T and -S are available for all types. Replace -N by -T or -S on type number, e.g. 24 ISR10-05-S RoHS. Attention: Operation in extended temperature range requires an external input capacitor of 220 µF, connected as close as possible to the input pins.

Modifications and customs are available upon request.

<sup>1</sup> For 33% load variation with a rise/fall time of ≥ 50 µs

Dimensions in mm (inches)



Pin Configuration	
Pin	Single output
1	+Vi
2	-Gi
3	+Vo
4	-Go
5 <sup>5</sup>	i

<sup>5</sup> Optional inhibit pin. If required add i to type number, e.g. 24 ISR10-05-Ni RoHS.